PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Computer Sc. & Engg. Engg. Semester: 4th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: MPMC No. Of Days/per week class allotted 04

Semester From Date : 15.04.21 To Date : 13.08.21 No. Of Weeks : 18

Week	Class Day	Theory /Practical Topics
1	1.	Introduction to Microprocessor & Microcontroller
	2.	& distinguish between them.
2.	3	Concept of Address bus, data bus, control bus & System Bus.
		General Bus structure Block diagram.
	4	Basic Architecture of 8085 (8 bit) mp.
	5	Basic Architecture of 8085 (8 bit mp.
3	6	Pin diagram of 8085 mp.
	7	pin diagram of 8085 mp.
	8	Register Organization,
	9	Distinguish between SPR & GPR. Timing & Control Module
4	10	Stack, Stack pointer & Stack top
	11	Interrupts :- 8085 Interrupts,
	12	Masking of Interrupt (SIM, RIM)

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Computer Sc. & Engg. Engg. Semester: 4th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: MPMC No. Of Days/per week class allotted _____

Semester From Date : _____ To Date : _____ No. Of Weeks : _____

Week	Class Day	Theory /Practical Topics
5	13	Memory & I/o Addressing.
	14	Define opcode, operand, T-state, Fetch Cycle,
	15	Instruction cycle & discuss the concept of timing diagram.
6	16	Draw timing diagram for memory read, memory write
	17	Timing diagram for I/o read, I/o write machine cycle.
	18	Timing diagram for (MOV, MVI, LDA instruction)
7	19	Concept of interfacing
	20	Define Mapping & Data transfer mechanism —
	21	Memory & I/o Mapping.
8	22	Interfacing EPROM & RAM Memories
	23	Concept of Address decoding for I/o devices
	24	PPI : 8255 (Programmable peripheral Interface)
	25	ADC & DAC with Interfacing.

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Computer Sc. & Engg. Engg. Semester: 4th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: MPMC No. Of Days/per week class allotted_____

Semester From Date :_____ To Date :_____ No. Of Weeks : _____

Week	Class Day	Theory /Practical Topics
9	26	Addressing data & Differentiate between one byte, two byte & 3 byte instructions with example.
	27	Addressing Modes in instructions with suitable examples.
	28	Instruction set of 8085 (Data Transfer) Arithmatic, Logical
10	29	Branching, Stack & I/o, Machine Control.
	30	Simple Assembly language programming of 8085
	31	① Simple Addition & Subtraction, 1's & 2's complement
	32	② Masking of bits, Counter & Time delay
11	33	③ Looping, Counting & Indexing (call / JMP etc) Stack & Subroutine programes
	34	
	35	④ Code conversion, BCD Arithmatic & 16 bit data operation, Block Transfer, compare bet" two numbers, Largest & Smallest no. ina array

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Computer Sc. & Engg. Engg. Semester: 4th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: MPMC No. Of Days/per week class allotted_____

Semester From Date :_____ To Date :_____ No. Of Weeks : _____

Week	Class Day	Theory /Practical Topics
	36	Interfacing Seven Segment Displays
12	37	Generate Square waves on all lines of 8255
	38	Design Interface a traffic light control systems using 8255
	39	Design Interface for Stepper motor control using 8255
13	40	Register Organisation of 8086
	41	Register organisation(flags) of 8086.
	42	Internal architecture of 8086.
	43	Signal description of 8086
14	44	Signal description of 8086
	45	General Bus Operation & Physical Memory Organisation
	46	Minimum Mode & Timings
15	47	Maximum Mode & Timings
	48	Interrupts, ISR, NMI, Maskable Interrupt
	49	8086 Instruction set : Addressing Mode, Assembler Directives & Operators

Subhrasmita Bhoi
Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Computer Sc. & Engg. Engg. Semester: 4th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: MPMC No. Of Days/per week class allotted _____

Semester From Date : _____ To Date : _____ No. Of Weeks : _____

Week	Class Day	Theory /Practical Topics
	50	Simple Assembly language Programming of 8086
16	51	Distinguish between Microprocessor & Microcontroller & bit & 16 bit Microcontroller
	52	CISC & RISC Processor
	53	Architecture of 8051 Microcontroller
17	54	Signal description of 8051 Microcontroller
	55	Memory organisation-RAM structure, SFR
	56	Registers, timers, interrupts of 8051 Microcontroller
	57	Addressing Mode of 8051.
18	58	Simple 8051 Assembly language Programming Arithmatic & Logic instruction, JUMP, LOOP, CALL, I/o port Programming
	59	Interrupts, Timer & Counters
	60	Serial communication
	61	Microcontroller Interrupts & Interfacing to 8255.

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Section-A & [Section-B]

Discipline: Electrical Engg. Semester: 5th Electrical

Name of the Teaching Faculty : Subhrasmita Bhai

Subject: DEMP No. Of Days/per week class allotted 02

Semester From Date : 01.09.20 To Date : 15.02.21 No. Of Weeks : 18

Week	Class Day	Theory /Practical Topics
1	1	Introduction to Microprocessor, Microcomputers
	2	Architecture of Intel 8085A Microprocessor
2	3	Description of each block.
	4	Pin diagram & description
	5	pin diagram & description
3	6	Stack, stack pinter & stack top
	7	Interrupts
	8	Discussion of block diagram & pin diagram
4	9	opcode & operand
	10	Differentiate between One byte, two byte & three byte instruction with example.

Subhrasmita Bhai
Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Electrical Engg. Semester: 5th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: DEMP No. Of Days/per week class allotted

Semester From Date : 01.09.20 To Date : 15.02.21 No. Of Weeks :

Week	Class Day	Theory /Practical Topics
5	11	Instruction set of 8085 example
	12	Addressing Mode
6	13	Fetch cycle, Machine cycle, instruction
	14	cycle, T-State
	15	Timing diagram for memory read,
	16	Memory Write.
7	17	Timing diagram for I/o read, I/o Write
		Timing diagram for 8085 instruction
	18	Dismission of Timing process of 8085 mp.
8	19	Counter & Time delay
	20	Simple assembly language programming of 8085
9	21	Programming of 8085

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner

PADMASHREE KRUTARTHA ACHARYA INSTITUTE OF ENGINEERING & TECHNOLOGY, BARGARH

LESSON PLAN

Discipline: Electrical Engg. Semester: 5th

Name of the Teaching Faculty : Subhrasmita Bhoi

Subject: DEMP No. Of Days/per week class allotted 02

Semester From Date : 01.09.20 To Date : 15.02.21 No. Of Weeks : 18

Week	Class Day	Theory /Practical Topics
10	22	Basic Interfacing concepts
	23	Memory Mapping & I/o Mapping
11	24	Functional block diagram of 8255
	25	programmable peripheral interface
12	26	Description of each block of 8255 PPI
13	27	Application using 8255 ; Seven segment
	28	LED display
14	29	Square wave generator
15	30	Traffic light Controller
	31	Discussion of Instruction set
16	32	Discussion of Interfacing & support chips.
17	33	Discussion of overall 8085 µp.
18	34	Overall Revision
	35	overall Revision

Subhrasmita Bhoi

Signature of the Faculty

Scanned with CamScanner